

REMARKS

Claims 1-6 are currently pending in the application. Claims 1 and 6 are in independent form. Claim 1 has been amended. No claims are canceled. Claim 6 is newly added in this response.

Rejections under 35 U.S.C. § 102(e)

The Office Action rejects claims 1-3 and 5 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 7,161,576 issued to Kawabe, et al (hereinafter "Kawabe").

To anticipate a claim under § 102, a single prior art reference must disclose every element of the claim. MPEP § 2131. Kawabe does not satisfy this objection.

Independent claim 1, as amended, recites in part:

A liquid crystal display ... comprising:

... means for varying, in accordance with the input image data and according to one of the modes for driving the liquid crystal display panel, a gradation voltage level to be applied to the liquid crystal display panel, so as to prevent, regardless of the hold display mode or the impulse display mode, changes in gamma characteristics due to differences in response speed of liquid crystal between display gradations, which differences are caused by insertion of the monochrome display data.

Since changes in the gamma characteristics are prevented, the gamma characteristics of the hold display mode are the same as the gamma characteristics in the impulse display mode.

Kawabe does not disclose prevention of changes in gamma characteristics between a hold-type scanning and impulse-type scanning. Rather, Kawabe merely discloses that the respective gamma characteristics for hold-type scanning and impulse type scanning are set independently. To correct the gamma characteristic Kawabe provides a "means for applying another gradation voltage for the impulse type scanning." Col. 10, lines 49-55. More specifically, Kawabe discloses in Fig. 5 and at col. 11, lines 2-26 that gradation voltage bus 504

transmits a gradation voltage for hold-type scanning that is generated by the ladder resistor 502. A separate gradation voltage bus 505 transmits a gradation voltage for impulse-type scanning that is generated by the ladder resistor 503. Analog switch 506 switches between the two gradation voltage paths depending on either hold-type scanning or impulse-type scanning, setting the gamma characteristics for the hold-type scanning and the impulse type scanning, respectively. Hence, Kawabe does not disclose that the respective gamma characteristics for hold-type scanning and impulse type scanning are the same.

Accordingly, Applicant submits that independent claim 1, as amended, is not anticipated by the applied art. Claims 2-3 and 5 depend from claim 1 and are therefore believed to be in condition for allowance for at least the same reasons as claim 1. Reconsideration of the claims and withdrawal of the rejection are respectfully requested.

Rejection under 35 U.S.C. § 103(a)

Claim 4 is rejected under 35 U.S.C. § 103(a) as being unpatentable over Kawabe in further view of U.S. Patent No. 7,084,861 issued to Iisaka. This rejection is respectfully traversed.

Claim 4 depends from independent claim 1, which, as discussed above, is believed to be in condition for allowance. Applicant respectfully submits that claim 4 is in condition for allowance for at least the same reasons as the base claim from which it depends.

Additionally, neither Iisaka nor Kawabe discloses that "the means for varying the gradation voltage level to be supplied to the liquid crystal display panel varies the gradation voltage in further accordance with the detected temperature in the display." Instead, Iisaka discloses varying "a pulse width of the pulse signals for bringing the pixels into the transmissive states as is predetermined in correspondence with each gradation ... on the basis of a detection output of the temperature detection means in each field." The pulse signals simply turn a pixel on and off. Col. 9, lines 24-50. Clearly, changing a pulse width of the pulse signal is not the

same as "varying a gradation voltage level." Reconsideration of claim 4 and withdrawal of the rejection are requested.

New Claim

Applicant respectfully requests consideration of newly presented independent claim 6. Applicants submit that the applied art of record does not disclose every feature of claim 6. In particular, neither Kawabe or Iisaka discloses or suggests at least an impulse drive mode that includes "sequentially writing the input image data in each of scan lines of the liquid crystal display panel."

CONCLUSION

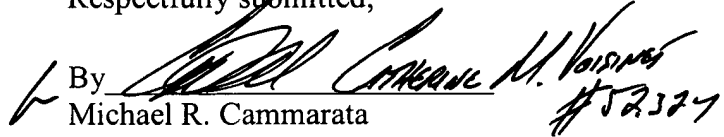
In view of the above remarks, it is believed that claims 1-6 are in condition for allowance.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Michael R. Cammarata, Reg. No. 39,491 at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37.C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

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Respectfully submitted,

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